

ABSTRACT OF THE DISCLOSURE

The invention uses: a clock signal generator for generating a clock signal asynchronized with an input signal; a frequency dividing unit for frequency dividing the clock signal outputted from the clock signal generator and outputting a predetermined clock enable signal; a digital interface processor for separating and outputting a compressed video image, audio information, and the like from a compressed digital signal in response to the clock enable signal outputted from the frequency dividing unit; a video processing unit for decoding the compressed video information outputted from the digital interface processor, obtaining a video signal, and synchronizing it with the input signal; and an audio processing unit for decoding the audio information outputted from the digital interface processor, obtaining a audio signal, and outputting the audio signal at synchronous timing according to an audio operating mode.

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